1 3
2 3
2.1 V1 V2 3
2.2 V1 4
2.2.1 V1 4
2.2.2 V1 5
2.2.3 CPU 5
2.2.4 CLR_CMOS 6
2.2.5 7
2.3 V2 9
2.3.1 V2 9
2.3.2 V2 10
2.3.3 11
2.3.4 CMOS 12
3 12
3.1 12
3.1.1 13
3.1.2 14
3.2 14
3.2.1 14
3.2.2 15
3.3 16
3.3.1 16
3.3.2 16
3.4 16
3.4.1 16
3.4.2 19
3.5 19
3.5.1 19
3.5.2 19
3.6 20
3.6.1 20
3.6.2 20
3.7 20
3.7.1 20
3.7.2 21
4 21

1

RCD6000 RCD4500 RCD3000 RCD6000 V2 RCD4500 V2 RCD3000 V2

2

V1 V2

2.1 V1 V2

V1 V2 V2 V1

2.1

2.2 V2 RCD6000/4500/3000 V2 log V2

2.2 V1

2.2.1 V1

2.3V1

2.2.2 V1

2.4 V1

2.2.3 CPU

/ 1 CHA_0 CHE_0

2.2.4 CLR_CMOS

2.5 CMOS

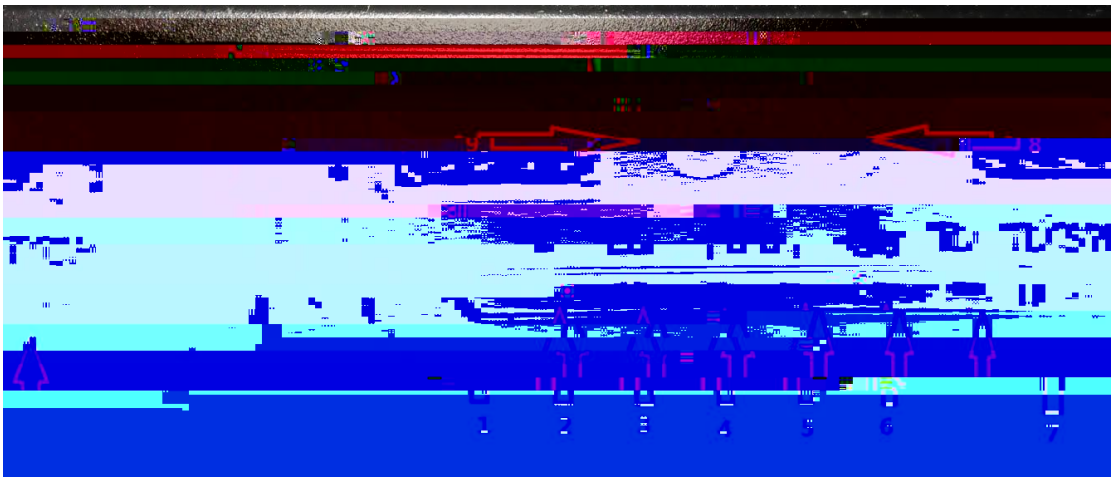
CLR_CMOS CMOS

Pin1-2

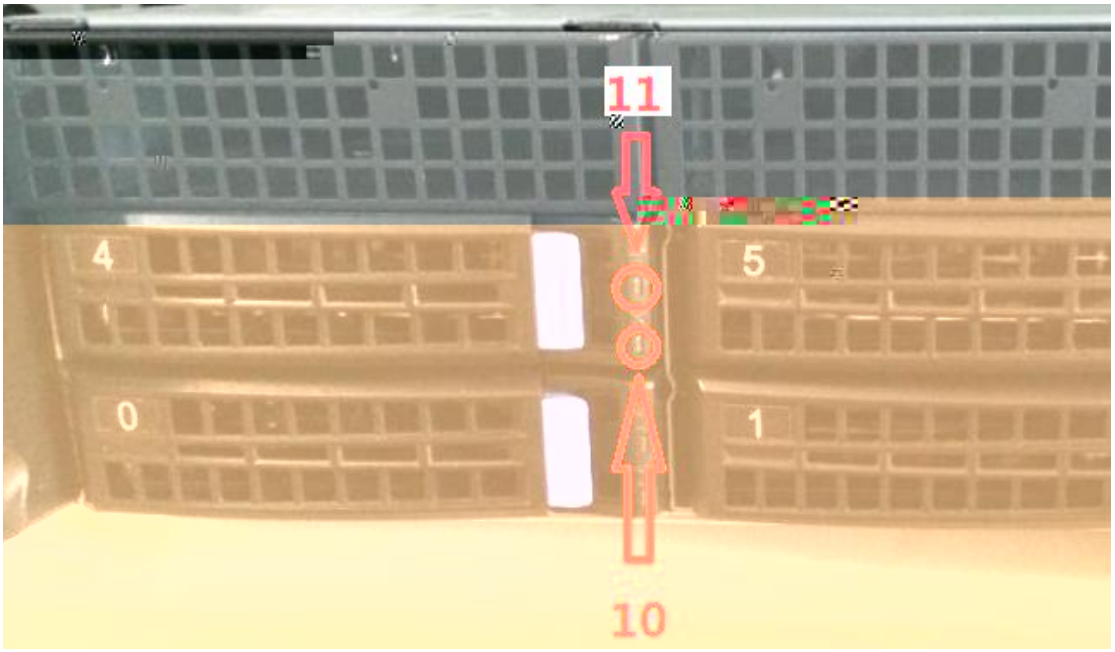
Pin2-3 CMOS

2.5

2.2.5



2.6 V1



2.7 V1

2.3 V1

1		
2		( ) AC BMC
3	1	Intel I350 1
4	2	Intel I350 2
5		AC BMC
6	ID	ID
7	ID	ID
8	Power	
9		
10		( ) RAID Rebuilding ( ) Locate
11		

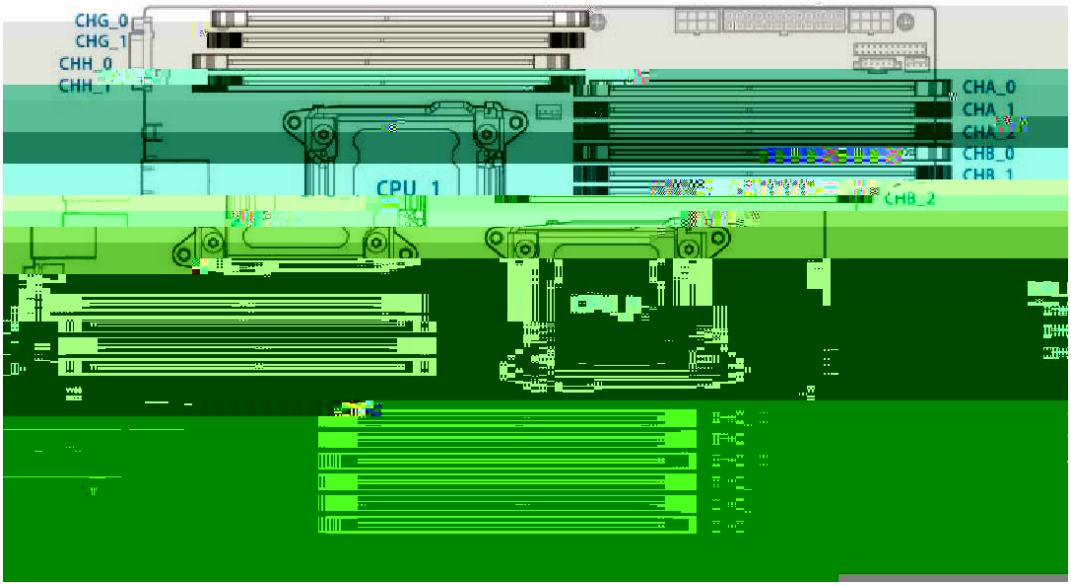
2.3 V2

2.3.1 V2

19	
20	(CPU1
21	CPU1
22	CPU0
23	8pin
24	24pin
25	PMBUS
26	

2.3.2 V2

V2 2.9



2.9 V2

:

:

CHB_1/CHC_1/CHD_1/ CHA_2/ „ „

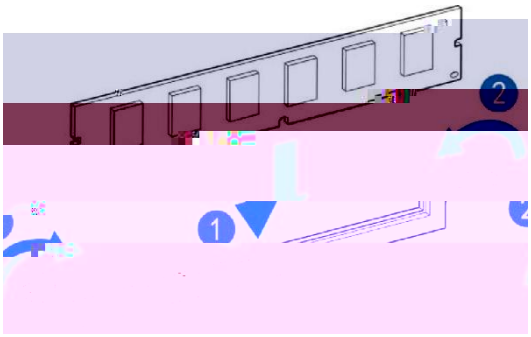
CHD_0,CHA_1 „ „

„ „

1:

2: , ,

,



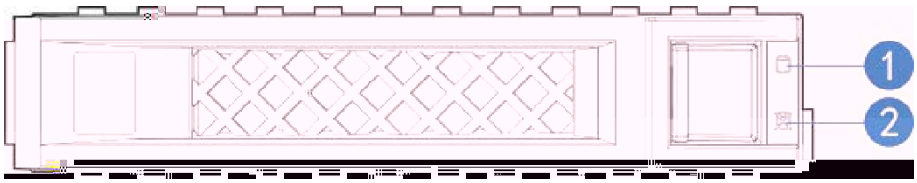
2.10

2.3.3

1

2.11 V2

2.5 V2


1
2

3.1.1

ping VGA

“ ”

ping

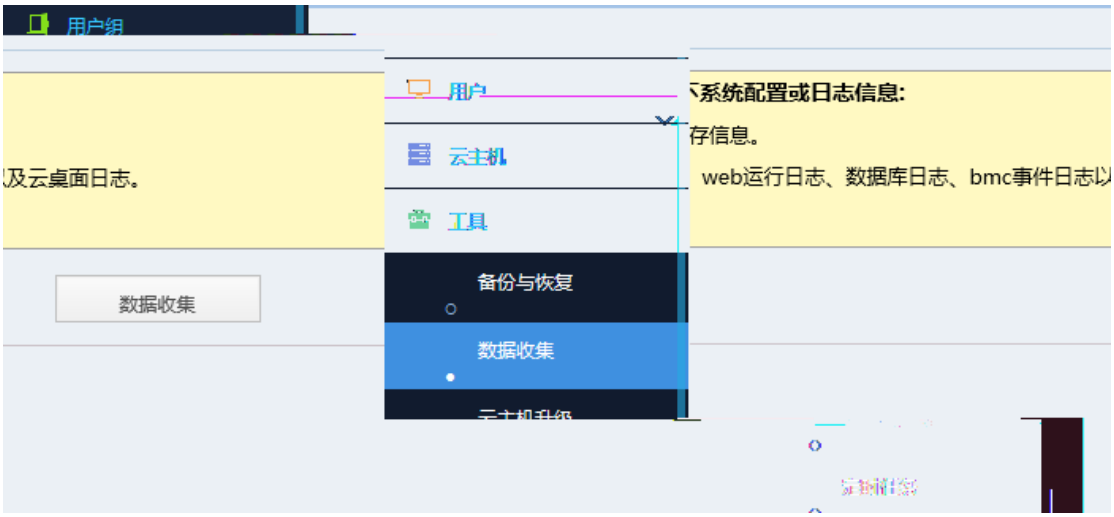
V1 V2 V1

V2 web 3.1 /

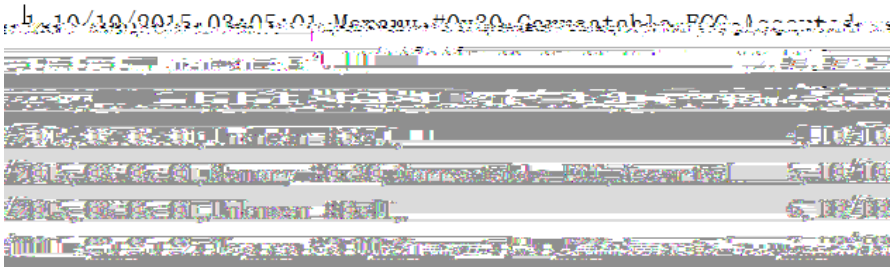
“ rcd_info_collect.tar.gz”

rcd_info_collect\bmc\bmc.log bmc

“ CorrectableECC Asserted” ECC 3.2



3.1



3.2 bmc ECC

V1 V2

V1 V2 web BMC

ECC ECC

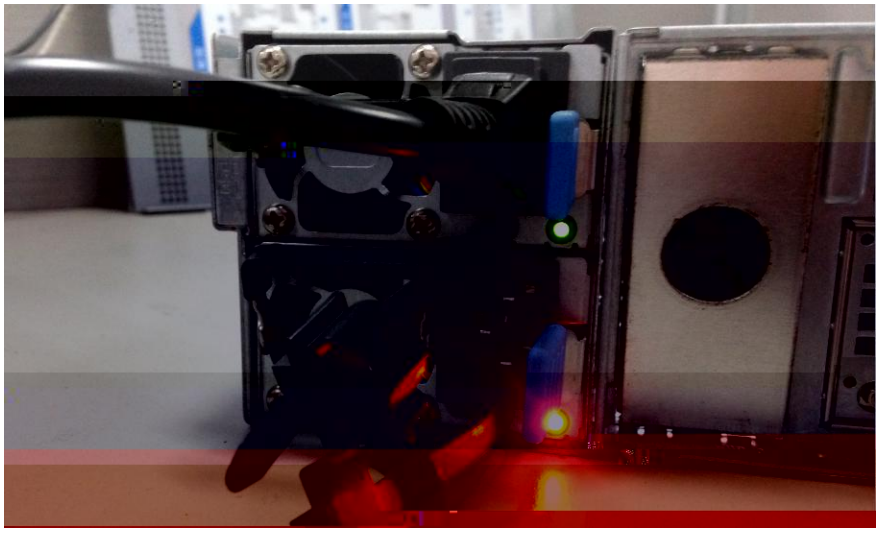
ECC

3.1.2

1 VGA

2

3.3 V1



3.4 V2

3.2.2

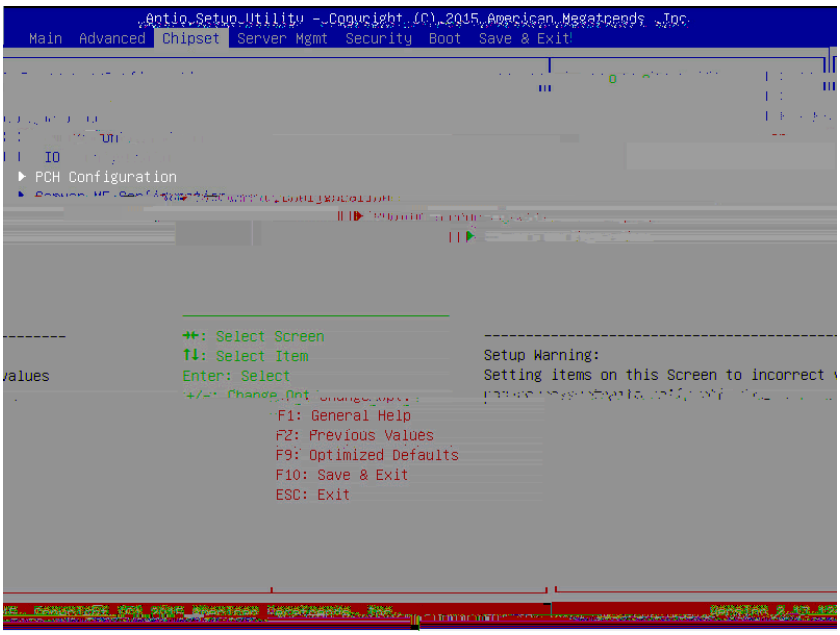
1
2
3

4
5
6
7

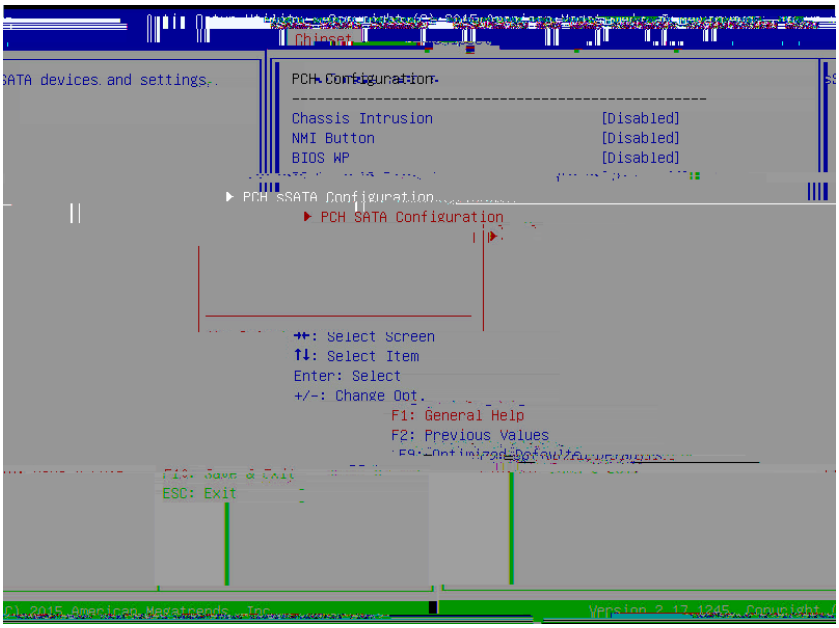
8
9 LED

2 V2 Chipset PCH -> PCH-sSATA Configuration,
sSATA Port0 sSATA Port1 sSATA Port2 sSATA Port3

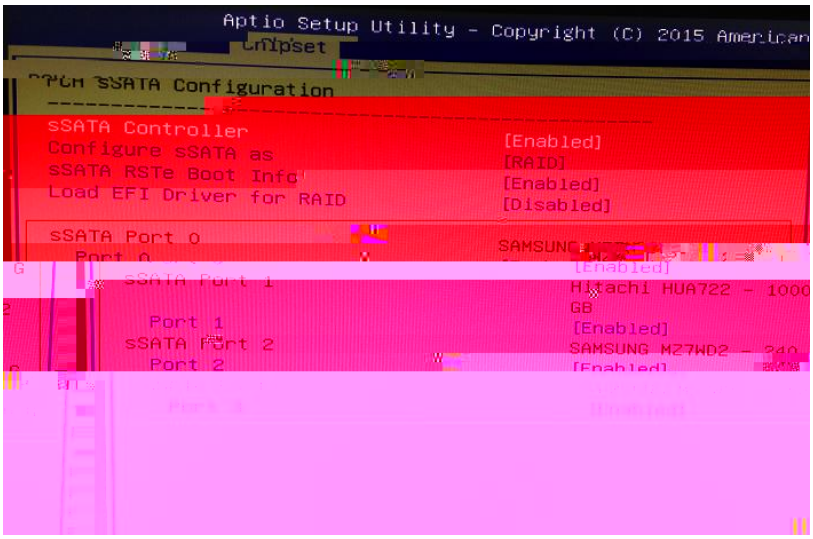
3.8 3.9 3.10



3.8 V2 bios sata



3.9 V2 bios sata



3.10 V2 bios sata

3.4.2

1

SSD

2

3 bios

4 BMC BMC
BMC

3.5

3.5.1

ping

IP

3.5.2

3.6

3.6.1

3.7.2

ping web
bios

1
2
3

4
5

6
cmos
CLR_CMOS 1-2 2-3 10s

7

4

1 CPU
2
3
4
5
6
7
1-5 1-4

1 CPU 2 E5 CPU
Intel CPU
CPU
2

USB COM

CMOS

CMOS

3